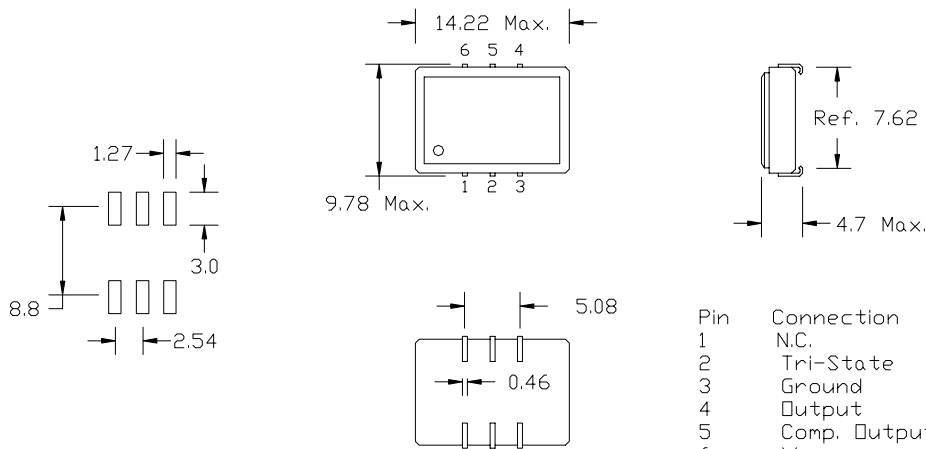




SMD Oscillator, LVPECL / LVDS  
Ceramic Package, 9 mm x 14 mm

ISM68 Series

<b>Frequency</b>	1.000 MHz to 750.000 MHz	
<b>Output Level</b>	LVDS	PECL
<b>Level</b>	Vod = 393 mV Typ., 475 mV Max.	'0' = Vcc-1.63 VDC Max. '1' = Vcc - 1.02 VDC Min.
<b>Duty Cycle</b>	Specify 50% ± 10% or ± 5% See Table	
<b>Rise / Fall Time</b>	0.6 nS Max.	
<b>Output Load</b>	100 Ω Differential	50 Ω to Vcc - 2.0 VDC
<b>Frequency Stability</b>	See Frequency Stability Table (Includes room temperature tolerance and stability over operating temperature)	
<b>Start-up Time</b>	10 mS Max.	
<b>Enable / Disable Time</b>	100 nS Max.	
<b>Supply Voltage</b>	3.3 VDC ±10%	
<b>Current</b>	130 mA Max. **	
<b>Temperature</b>		
<b>Operating</b>	See Operating Temperature Table	
<b>Storage</b>	-55° C to +125° C	
<b>Environmental / Tape and Reel</b>	See Appendix B for Environmental information, Appendix C for Tape and Reel information	
<b>Package Information</b>	MSL = N.A., Termination = e4	



Tri-State Function	
Pin 2 Open	Enable
Pin 2 ≥ 70% Vdd	Enable
Pin 2 ≤ 30% Vdd	Disable

Dimension Units: mm

Part Number Guide		Sample Part Number: ISM68 - 3169BH - 156.250 MHz					
Package	Input Voltage	Operating Temperature	Symmetry (Duty Cycle)	Output	Stability (in ppm)	Enable / Disable	Frequency
ISM68 -	3 = 3.3 V	1 = 0° C to +70° C	5 = 45 / 55 Max.	8 = LVDS	*D = ±15	H = Enable	- 156.250 MHz
		6 = -10° C to +70° C	6 = 40 / 60 Max.	9 = PECL	F = ±20		
		3 = -20° C to +70° C			A = ±25		
		4 = -30° C to +75° C			B = ±50		
		2 = -40° C to +85° C			C = ±100		

NOTE: A 0.01 µF bypass capacitor is recommended between Vcc (pin 6) and GND (pin 3) to minimize power supply noise.  
 \* Not available for all temperature ranges. \*\* Frequency, supply, and load related parameters.