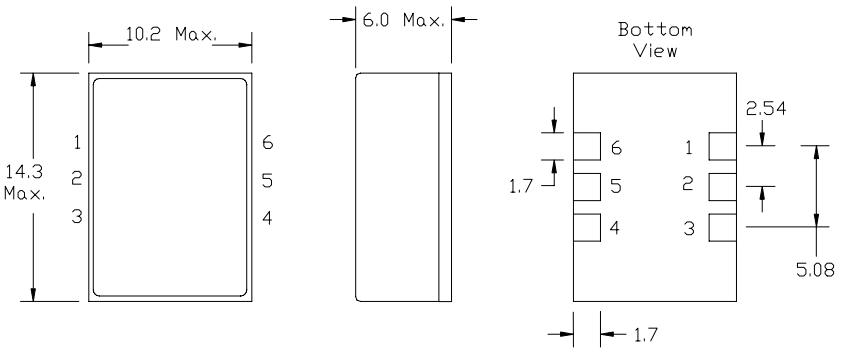
SMD Oscillator, LVPECL / LVDS

ISM65 Series

FR-4, 9 mm x 14 mm

Frequency	1.000 MHz to 750.000 MHz	
Output Level	LVDS	PECL
Level	Vod = 393 mV Typ., 475 mV Max.	'0' = Vcc - 1.63 Max., '1'= Vcc - 1.02 V Min.
Duty Cycle	Specify 50% ± 10% or ± 5% See Table	
Rise / Fall Time	0.6 nS Max.	
Output Load	100 Ω Differential	50 Ω to Vcc – 2.0 VDC
Frequency Stability	See Frequency Stability Table (Includes room temperature tolerance and stability over operating temperature)	
Start-up Time	10 mS Max.	
Enable / Disable Time	100 nS Max.	
Jitter	2 pS RMS Max., 12 KHz to 20 MHz @ 155.520 MHz	
Supply Voltage	3.3 VDC ±10%	
Current	130 mA Max. **	
Temperature		
Operating	See Operating Temperature Table	
Storage	-55° C to +125° C	
Environmental / Tape and Reel	See Appendix B for Environmental information, Appendix C for Tape and Reel information	
Package Information	MSL = 2a, Termination = e2 or e4	



Dimension Units: mm

Pin	Connection
1	N.C.
2	Enable
3	GND
4	Output
5	Comp. Output
6	Vcc

Tri-State Function	
Pin 2 Open	Enable
Pin 2 ≥ 2.0 VDC	Enable
Pin 2 ≤ 0.8 VDC	Disable

Part Number Guide **Sample Part Number: ISM65 - 3169BH - 156.250**

Package	Input Voltage	Operating Temperature	Symmetry (Duty Cycle)	Output	Stability (in ppm)	Enable / Disable	Frequency
ISM65 -	3 = 3.3 V	1 = 0° C to +70° C	5 = 45 / 55 Max.	8 = LVDS	*D = ±15	H = Enable	- 156.250 MHz
		6 = -10° C to +70° C	6 = 40 / 60 Max.	9 = PECL	F = ±20		
		3 = -20° C to +70° C			A = ±25		
		4 = -30° C to +75° C			B = ±50		
		2 = -40° C to +85° C			C = ±100		

NOTE: A 0.01 µF bypass capacitor is recommended between Vcc (pin 4) and Gnd (pin 2) to minimize power supply noise.
* Not available for all temperature ranges. ** Frequency, supply, and load related parameters.