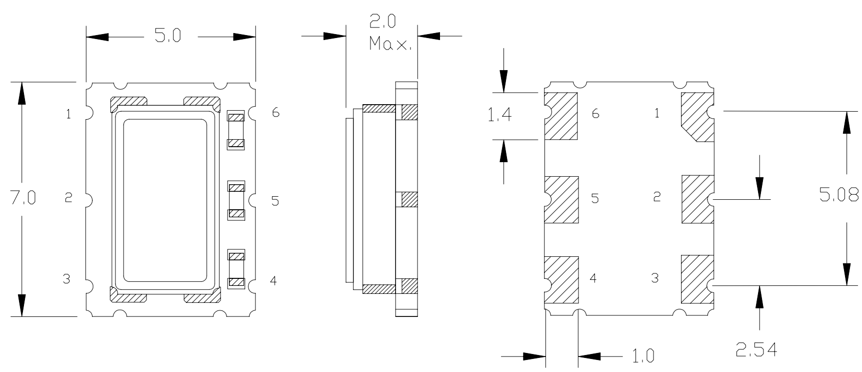
SMD Oscillator, PECL or LVDS
Ceramic Package, 5 mm x 7 mm

ISM99 Series

Frequency	1.000 MHz to 800.000 MHz	
Output Level	LVDS	PECL
Level	Vod = 393 mV Typ., 475 mV Max.	‘0’ = Vcc - 1.63 V Max. ‘1’ = Vcc - 1.02 V Min.
Duty Cycle	Specify 50% ± 10% or ± 5% See Table	
Rise / Fall Time	0.6 nS Max.	
Output Load	100 Ω Differential	50 Ω to Vcc - 2.0 VDC
Frequency Stability	See Frequency Stability Table (Includes room temperature tolerance and stability over operating temperature)	
Start-up Time	10 mS Max.	
Enable / Disable Time	100 nS Max.	
Supply Voltage	3.3 VDC ±10%	
Current	90 mA Max.	130 mA Max.
Temperature		
Operating	See Operating Temperature Table	
Storage	-55° C to +125° C	
Environmental	See Appendix B for information	
Package Information	MSL = N.A., Termination = e4	



Tri-State Function	
Pin 1 Open	Enable
Pin 1 ≥ 70% Vdd	Enable
Pin 1 ≤ 30% Vdd	Disable

Pin	Connection
1	Enable
2	N.C.
3	GND
4	Output
5	Comp. Output
6	Vcc

Dimmension Units: mm

Part Number Guide **Sample Part Number:** **ISM99 - 3159BH - 156.250 MHz**

Package	Input Voltage	Operating Temperature	Symmetry (Duty Cycle)	Output	Stability (in ppm)	Enable / Disable	Frequency
ISM99 -	3 = 3.3 V	1 = 0° C to +70° C	5 = 45 / 55 Max.	8 = LVDS	*A = ±25	H = Enable	- 156.250 MHz
	7 = 3.0 V	6 = -10° C to +70° C	6 = 40 / 60 Max.	9 = PECL	B = ±50		
	2 = 2.7 V	3 = -20° C to +70° C			C = ±100		
	6 = 2.5 V	4 = -30° C to +75° C					
		2 = -40° C to +85° C					
		7 = 0° C to +60° C					

NOTE: A 0.01 µF bypass capacitor is recommended between Vcc (pin 6) and Gnd (pin 3) to minimize power supply noise.
* Not available for all temperature ranges. ** Frequency, supply, and load related parameters.