

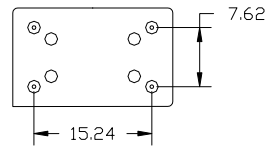
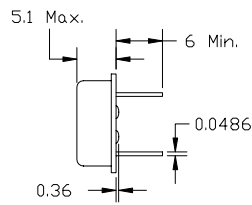
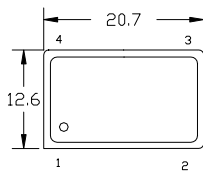


Leaded Oscillator, VCXO, TTL / HC-MOS
Metal Package, Full Size DIP and Half DIP

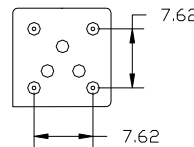
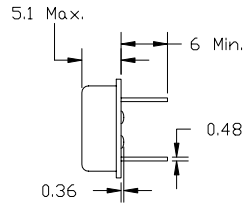
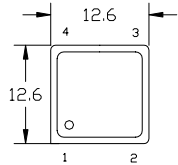
I202 / I203 Series

Frequency	1.000 MHz to 170.000 MHz	
Output Level	TTL	HC-MOS
Level	'0' = 0.4 VDC Max., '1' = 2.4 VDC Min.	'0' = 0.1 Vcc Max., '1' = 0.9 Vcc Min.
Duty Cycle	50% ± 5%	
Rise / Fall Time	10 nS Max. *	
Output Load	Fo < 50 MHz = 10 TTL, Fo > 50 MHz = 5 LSTTL	15 pF
Frequency Stability	See Frequency Stability Table	
Supply Voltage	See Supply Voltage Table, tolerance ±10 %	
Current	50 mA Max. *	
Control Voltage	1.65 VDC ± 1.5 VDC for Vcc = 3.3 VDC, 2.5 VDC ± 2.0 VDC for Vcc = 5.0 VDC	
Slope	Positive	
Temperature		
Operating	See Operating Temperature Table	
Storage	-55° C to +125° C	
Environmental	See Appendix B for information	
Package Information	MSL = N.A., Termination = e1	

I203 Package



I202 Package



Dimension Units: mm

- Pin Connection
- 1 Control Voltage
- 2 GND
- 3 Output
- 4 Vcc

Part Number Guide		Sample Part Number: I203 - 1BC3H - 27.000			
Package	Operating Temperature	Frequency Stability	Pullability	Supply Voltage	Frequency
I202 - I203 -	1 = 0° C to +70° C	F = ±20 ppm **	B = ± 50 ppm Min.	5 = 5.0 VDC	- 27.000 MHz
	3 = -20° C to +70° C	X = ±30 ppm	C = ± 100 ppm Min.	3 = 3.3 VDC	
	4 = -30° C to +75° C	B = ±50 ppm	K = ± 150 ppm Min.		
	2 = -40° C to +85° C	C = ±100 ppm	L = ± 200 ppm Min.		

NOTE: A 0.01 µF bypass capacitor is recommended between Vcc (pin 4) and Gnd (pin 2) to minimize power supply noise.
* Frequency, supply, and load related parameters. ** Not available for all temperature ranges.