

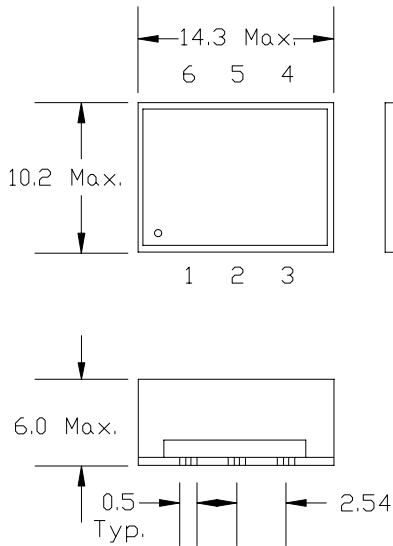


SMD Oscillator, VCXO, PECL / LVDS
FR-4 Package, 10 mm x 14 mm

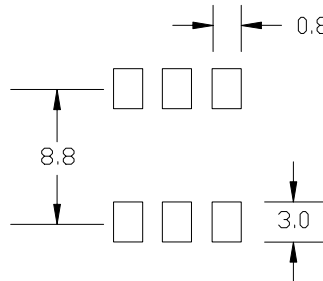
I606 Series

Frequency	1.000 MHz to 750.000 MHz	
Output Level	LVDS	PECL
Level	Vod = 393 mV Typ., 475 mV Max.	'0' = Vcc - 1.63 VDC Max. '1' = Vcc - 1.02 VDC Min.
Duty Cycle	50% ± 10%	
Rise / Fall Time	0.6 nS Max.	
Output Load	100 Ω Differential	50 Ω to Vcc - 2.0 VDC
Frequency Stability	±50 ppm Max.	
Supply Voltage	3.3 VDC ±10%	
Current	90 mA Max.	130 mA Max.
Control Voltage	1.65 VDC ± 1.5 VDC	
Linearity	±10%	
Temperature		
Operating	See Operating Temperature Table	
Storage	-55° C to +125° C	
Environmental	See Appendix B for information	
Package Information	MSL = 2a, Termination = e2 or e4	

Dimension Units: mm



Recommended pad layout



Pin	Connection
1	Voltage Control
2	Tristate
3	GND
4	Output
5	Comp. Output
6	Vcc

Tri-State Function	
Pin 2 Open	Enable
Pin 2 ≥ 70% Vdd	Enable
Pin 2 ≤ 30% Vdd	Disable

Part Number Guide		Sample Part Number: I606 - 1BC3H - 155.520				
Package	Operating Temperature	Frequency Stability	Pullability	Output	Enable / Disable	Frequency
I606 -	1 = 0° C to +70° C	D = ±15 ppm *	B = ± 50 ppm Min.	8 = LVDS	H = Enable	- 155.520 MHz
	3 = -20° C to +70° C	A = ±25 ppm	C = ± 100 ppm Min.	9 = PECL		
	2 = -40° C to +85° C	B = ±50 ppm				
		C = ±100 ppm				

NOTE: A 0.01 µF bypass capacitor is recommended between Vcc (pin 6) and GND (pin 3) to minimize power supply noise.
* Not available for all temperature ranges.