

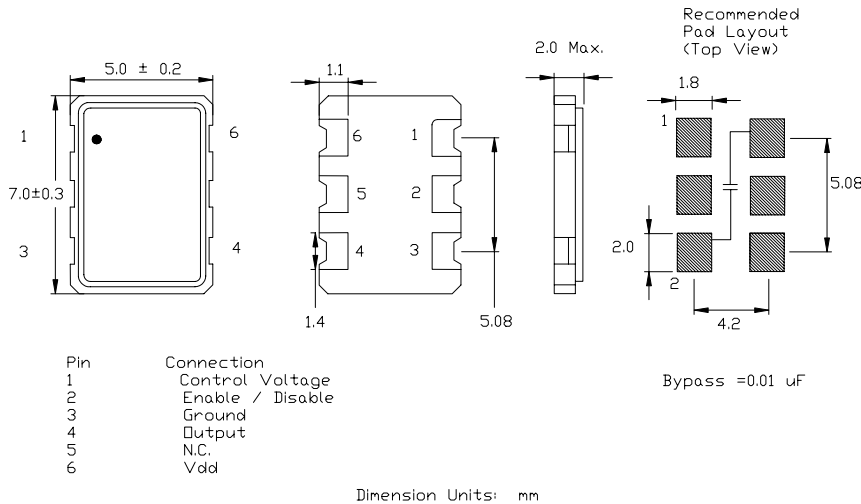


SMD Oscillator, VCXO, TTL / HC-MOS  
Ceramic Package, 5 mm x 7 mm

I605 Series

Frequency	1.000 MHz to 170.000 MHz	
Output Level	TTL	HC-MOS
Level	'0' = 0.4 VDC Max., '1' = 2.4 VDC Min.	'0' = 0.1 Vcc Max., '1' = 0.9 Vcc Min.
Duty Cycle	50% ± 5%	
Rise / Fall Time	10 nS Max.	
Output Load	15 pF, Fo < 50 MHz = 10 TTL, Fo > 50 MHz = 5 LSTTL	
Frequency Stability	±50 ppm Max.	
Supply Voltage	See Input Voltage Table, tolerance ±5 %	
Current	50 mA Max. **	
Control Voltage	1.65 VDC ± 1.5 VDC for Vcc = 3.3 VDC, 2.5 VDC ± 2.0 VDC for Vcc = 5.0 VDC	
Temperature		
Operating	See Operating Temperature Table	
Storage	-55° C to +125° C	
Environmental	See Appendix B for information	
Package Information	MSL = N.A., Termination = e4	

Tri-State Function	
Pin 2 Open	Enable
Pin 2 ≥ 70% Vdd	Enable
Pin 2 ≤ 30% Vdd	Disable



Part Number Guide		Sample Part Number: I605 - 1BC3H - 20.000				
Package	Operating Temperature	Frequency Stability	Pullability	Supply Voltage	Enable / Disable	Frequency
I605 -	1 = 0° C to +70° C	*D = ±15 ppm	B = ± 50 ppm Min.	5 = 5.0 VDC	H = Enable	- 20.000 MHz
	3 = -20° C to +70° C	A = ±25 ppm	C = ± 100 ppm Min.	3 = 3.3 VDC		
	2 = -40° C to +85° C	B = ±50 ppm				
		C = ±100 ppm				

NOTE: A 0.01 µF bypass capacitor is recommended between Vcc (pin 6) and GND (pin 3) to minimize power supply noise.  
\* Not available for all temperature ranges. \*\* Frequency, supply, and load related parameters.